B?A

1	GND					
2	GND					
3	5V					
4	5V					
5	JTAG_TMS	SAI2.MCLK	GPIO1.IO[11]			
6	JTAG_TRST	SAI2.TX_DATA	GPIO1.IO[15]	PWM8.OUT		
7	JTAG_TCK	SAI2.RX_DATA	GPIO1.IO[14]	PWM7.OUT		
8	JTAG_TDO	SAI2.TX_SYNC	GPIO1.IO[12]			
9	JTAG_MOD		GPIO1.IO[10]			
10	JTAG_TDI	SAI2.TX_BCK	GPIO1.IO[13]	PWM6.OUT		
11	GPIO_9	PWM2.OUT	GPIO1.IO[9]	CSI.HSYNC	SD2.RESET_B	SD1.RESET_B	UART5.CTS_B
12	GPIO_4	PWM3.OUT	GPIO1.IO[4]	USB.OTG1_PWR	SD1.RESET_B		UART5.TX
13	GPIO_1	I2C2.SDA	GPIO1.IO[1]	USB.OTG1_OC			
14	GPIO_0	I2C2.SCL	GPIO1.IO[0]				
15	CLK1_P						
16	CLK1_N						
17	GPIO_8	PWM1.OUT	GPIO1.IO[8]	CSI.VSYNC	SD2.VSELECT	UART5.RTS_B	
18	GPIO_5	PWM4.OUT	GPIO1.IO[5]	CSI.FIELD	SD1.VSELECT	UART5.RX	
19	UART1_TXD	I2C3.SCL	GPIO1.IO[16]	UART1.TX	ENET1.RDATA2	CSI.DATA[2]	UART5.TX
20	UART1_RXD	I2C3.SDA	GPIO1.IO[17]	UART1.RX	ENET1.RDATA[2]	CSI.DATA[3]	UART5.RX
21	UART3_RTS	CAN1.RX	GPIO1.IO[27]	UART3.RTS_B	ENET2.TX_ER	CSI_DATA[11]	
22	UART3_CTS	CAN1.TX	GPIO1.IO[26]	UART3.CTS_B	ENET2.RX_CLK	CSI_DATA[10]	
23	GND						

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B?B

24	UART5_RX	GPIO1.IO[31]	ENET2.COL	I2C2.SDA	CSI.DATA[15]	ESPI2_MISO	
25	UART5_TX	GPIO1.IO[30]	ENET2.CRS	I2C2.SCL	CSI.DATA[14]	ECSPI2_MOSI	
26	RXI1_N						
27	RXI1_P						
28	TXO1_N						
29	TXO1_P						
30	UART4_TX	GPIO1.IO[18]	ENET2.TDATA[2]	I2C1.SCL	CSI.DATA[12]	ECSPI2_SCLK	
31	UART4_RX	GPIO1.IO[29]	ENET2.TDATA[3]	I2C1.SDA	CSI.DATA[13]	ECSPI2_SS0	
32	NAND_DQS	GPIO4.IO[16]	CSI.FIELD	QSPIA_SS0	PWM5.OUT		
33	NAND_WP	GPIO4.IO[11]	SD1.RESET_B	QSPIA_SCLK	PWM4.OUT	ECSPI3_RDY	
34	NAND_CE1	GPIO4.IO[14]	SD1.DATA6	QSPIA_DATA[2]	ECSPI3_MOSI	UART3.CTS_B	
35	NAND_CE0	GPIO4.IO[13]	SD1.DATA5	QSPIA_DATA[1]	ESPI3_SCLK	UART3.RX	
36	NAND_CLE	GPIO4.IO[15]	SD1.DATA7	QSPIA_DATA[3]	ECSPI3_MISO	UART3.RTS_B	
37	NAND_READY	GPIO4.IO[12]	SD1.DATA4	QSPIA_DATA[0]	ECSPI3_SS0	UART3.TX	
38	UART1_RTS	GPIO1.IO[19]	ENET1.TX_ER	SD1.CD_B	CSI.DATA[5]	SD2.CD_B	UART5.RTS_B
39	SD1_DATA0	GPIO2.IO[18]	SD1.DATA0	SAI2.TX_SYNC	CAN1.TX		
40	SD1_DATA3	GPIO2.IO[21]	SD1.DATA3	SAI2.TX_DATA	CAN2.RX		
41	SD1_DATA1	GPIO2.IO[19]	SD1.DATA1	SAI2.TX_BCLK	CAN1.RX	USB.OTG2_PWR	
42	SD1_CMD	GPIO2.IO[16]	SD1.CMD	SAI2.RX_SYNC		USB.OTG1_PWR	
43	SD1_DATA2	GPIO2.IO[20]	SD1.DATA2	SAI2.RX_DATA	CAN2.TX	USB.OTG2_OC	
44	SD1_CLK	GPIO2.IO[17]	SD1.CLK	SAI2.MCLK		USB.OTG1_OC	
45	NAND_DATA7	GPIO4.IO[9]	SD2.DATA7	QSPIA.SS1_B	ECSPI4_SS0	UART2.RTS_B	
46	NAND_DATA6	GPIO4.IO[8]	SD2.DATA6	SAI2.RX_BCLK	ECSPI4_MISO	UART2.CTS_B	
47	NAND_DATA5	GPIO4.IO[7]	SD2.DATA5	QSPIB.DATA[3]	ECSPI4_MOSI	UART2.RX	
48	NAND_DATA4	GPIO4.IO[6]	SD2.DATA4	QSPIB.DATA[2]	ECSPI4_SCLK	UART2.TX	
49	NAND_DATA3	GPIO4.IO[5]	SD2.DATA3	QSPIB.DATA[1]	ECSPI4_SS3		
50	NAND_ALE	GPIO4.IO[10]	SD2.RESET_B	QSPIA_DQS	PWM3.OUT	ECSPI3_SS1	

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51	GND						
52	CSI_PIXCLK	GPIO4.IO[18]	SD2_WP	I2C1_SCL	UART6_RX		ESAI.TX2_RX3
53	CSI_DATA6	GPIO4.IO[27]	SD2_DATA6	ECSPI1_MOSI	SAI1_RX_DATA	SD1_RST	
54	CSI_DATA4	GPIO4.IO[25]	SD2_DATA4	ECSPI1_CLK	SAI1_TX_SYNC	SD1_WP	ESAI.TX_FS
55	CSI_DATA5	GPIO4.IO[26]	SD2_DATA5	ECSPI1_SS0	SAI1_TX_BCLK	SD1_CD_B	ESAI.TX_CLK
56	CSI_DATA7	GPIO4.IO[28]	SD2_DATA7	ECSPI1_MISO	SAI1_TX_DATA	SD1_VSELECT	ESAI.TX0
57	CSI_DATA1	GPIO4.IO[22]	SD2_DATA1	ECSPI2_SS0	SAI1MCLK	UART5_RX	ESAI.RX_HF_CLK
58	CSI_DATA0	GPIO4.IO[21]	SD2_DATA0	ECSPI2_CLK		UART5_TX	ESAI.TX_HF_CLK
59	CSI_DATA2	GPIO4.IO[23]	SD2_DATA2	ECSPI2_MOSI	SAI1_RX_SYNC	UART5_RTS	ESAI.RX_FS
60	CSI_DATA3	GPIO4.IO[24]	SD2.DATA3	ECSPI2_MISO	SAI1.RX_BCLK	UART5_CTS	
61	CSI_VSYNC	GPIO4.IO[19]	SD2_CLK	I2C2_SDA	PWM7.OUT	UART6_RTS_B	ESAI.TX4_RX1
62	CSI_HSYNC	GPIO4.IO[20]	SD2.CMD	I2C2_SCL	PWM8.OUT	UART6_CTS_B	ESAI.TX4_RX1
63	CSI_MCLK	GPIO4.IO[17]	SD2.CD_B	I2C1_SDA	UART6_TX		
64	GND						
65	NAND_DATA0	GPIO4.IO[2]	SD2.DATA0	QSPIB_SS1_B	ECSPI4.RDY		
66	NAND_nRE	GPIO4.IO[0]	SD2.CLK	QSPIB_SCLK	ECSPI3.SS2		
67	NAND_nWE	GPIO4.IO[1]	SD2.CMD	QSPIB_SS0_B	ECSPI3.SS3		
68	NAND_DATA1	GPIO4.IO[3]	SD2.DATA1	QSPIB_DQS	ECSPI4.SS1		
69	NAND_DATA2	GPIO4.IO[4]	SD2.DATA2	QSPIB_DATA[0]	ECSPI4.SS2		
70	LCD_DATA_10	GPIO3.IO[15]	SAI3.RX_SYNC	CSI.DATA[18]	CAN2.TX		
71	LCD_DATA_11	GPIO3.IO[16]	SAI3.RX_BCLK	CSI.DATA[19]	CAN2.RX		
72	LCD_DATA_1	GPIO3.IO[6]	PWM2.OUT	I2C3.SCL	SAI1.TX_SYNC		
73	LCD_ENABLE	GPIO3.IO[1]	UART4.RX	SAI3.TX_SYNC	ECSPI2.RDY		

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74	LCD_DATA_17	GPIO3.IO[22]	UART7.RX	CSI.DATA[0]	SD2.DATA[7]
75	RXI2_N				
76	RXI2_P				
77	TXO2_N				
78	TXO2_P				
79	USB_OTG2_VBUS				
80	USB_OTG2_D_N				
81	USB_OTG2_D_P				
82	POR_B				
83	GND				
84	TAMPER_9	GPIO5.IO[9]			
85	TAMPER_5	GPIO5.IO[5]			
86	ONOFF				
87	TAMPER_4	GPIO5.IO[4]			
88	TAMPER_1	GPIO5.IO[1]			
89	TAMPER_8	GPIO5.IO[8]			
90	TAMPER_0	GPIO5.IO[0]			
91	TAMPER_3	GPIO5.IO[3]			
92	TAMPER_7	GPIO5.IO[7]			
93	TAMPER_2	GPIO5.IO[2]			
94	USB_OTG1_VBUS				
95	TAMPER_6	GPIO5.IO[6]			
96	USB_OTG1_D_P				
97	USB_OTG1_D_N				

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